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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: BAKKER et al. Examiner: Choe, H.
Serial No.: 09/826,571 Group Art Unit: 2817
Filed: April 5, 2001 Docket No.: PHA 80-003A
(PENA.022C1)
Title: CIRCUIT, INCLUDING FEEDBACK, FOR REDUCING DC-OFFSET
AND NOISE PRODUCED BY AN AMPLIFIER

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this communication is being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on July 24, 2002.

By:

Kelly S. Waltigney

OFFICE ACTION RESPONSE AND AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated April 24, 2002, please consider the following amendments and remarks:

In The Claims

Please replace claim 8 and add new claims 15-17 as follows:

8. A circuit comprising: means for amplifying an input signal and producing a DC-offset and noise and for delivering an output signal to an output of the circuit; means, responsive to the amplifying means, for reducing the DC-offset and the noise produced by the amplifying means; and feedback means for further reducing the DC-offset produced by the amplifying means.

15. (New) The circuit of claim 1, wherein the feedback means is coupled between an input of the amplifier and an output of the means for reducing the DC-offset and the noise produced by the amplifier.

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